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Title
Concurrent **data** structures for hypercube machine.

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Abstract
To efficiently implement parallel algorithms on parallel computers, concurrent **data** structures (**data** structures which are simultaneously updatable) are needed. In this paper, three implementations of a priority queue on a distributed-memory **message** passing multiprocessor with a hypercube topology are presented. In the first implementation, a linear chain of processors is mapped onto the hypercube, and then a heap **data structure** is mapped onto the chain, where each processor stores one level in the heap. A similar approach is taken for the second implementation, but in this case, a banyan heap **data structure** is mapped onto the linear chain of processors. Again, each processor in the chain becomes responsible for one level of the **data structure.** For the third implementation, the banyan heap **data structure** is again used, but the **mapping** is not onto linear chain of processors. Instead, the banyan heap is mapped onto processors column by column, so that the algorithm can make better use of the concurrent processing capabilities of the hypercube topology in order to reduce bottlenecking in the first processor, an effect noted in the use of the linear chain employed by the first two implementations. The key advantage of banyan heap over the heap is that with banyan heap it is possible to retrieve elements at different percentile levels.

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